

Fig. 1

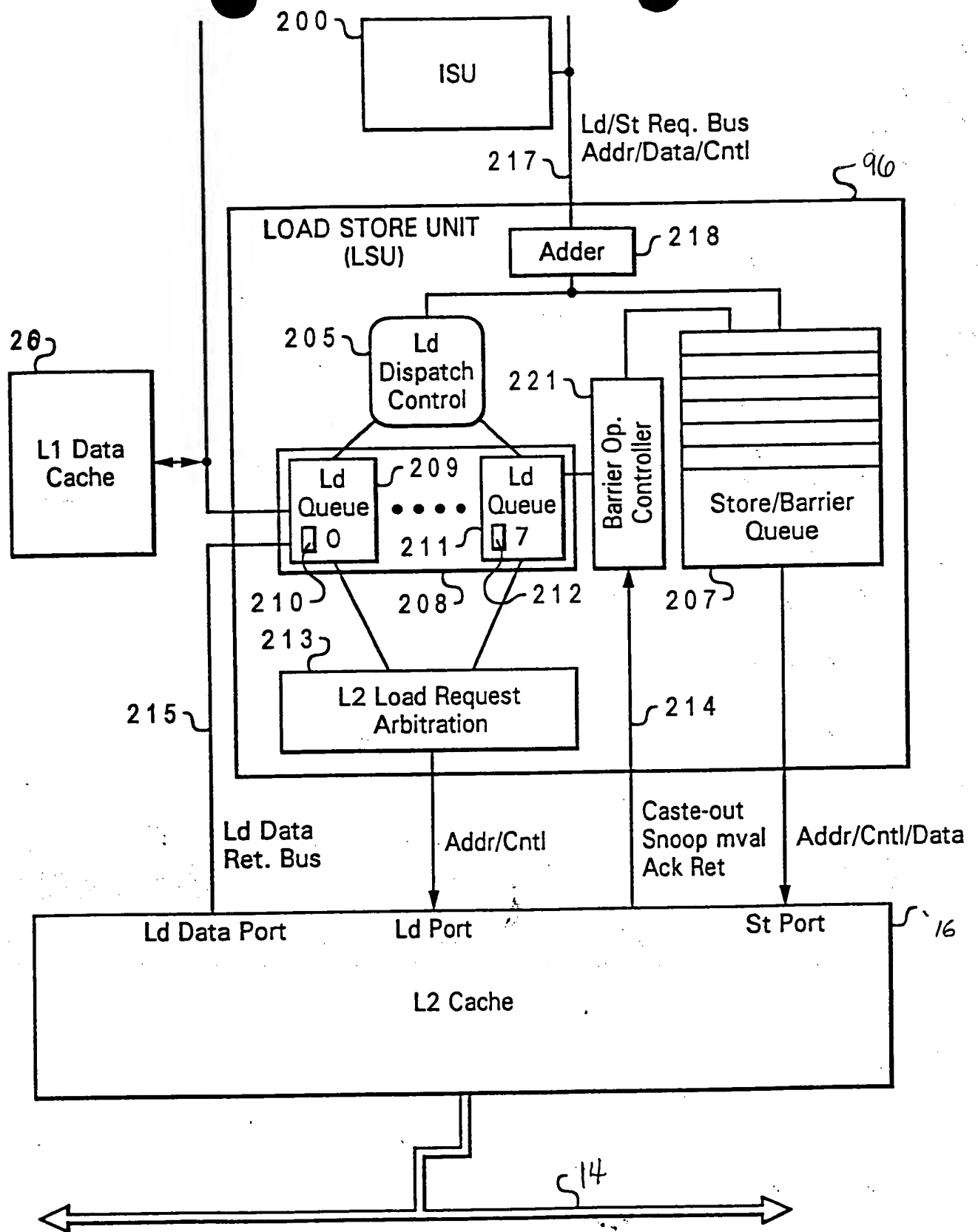
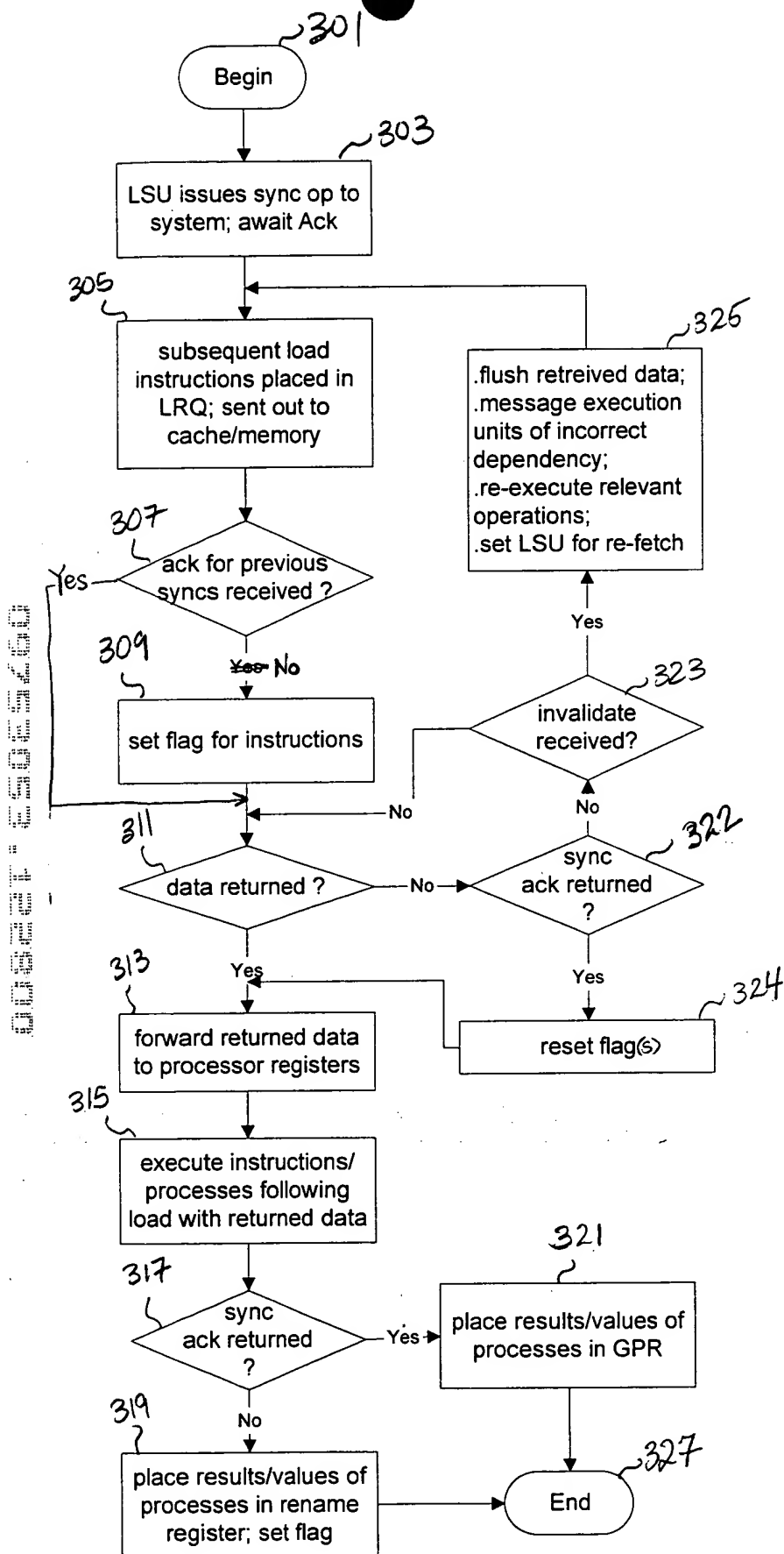


Fig. 2

Fig. 3



401

No.	Flag	Register rename entries (values)	GPR #
1	0		XXXXX0
2	1		XXX11
3	1		XXX10
.			
.			
.			
80	0		XX1XX

403

405

400

500

The diagram shows a sequence of frames over a timeline of 28 bits. The frames and their components are as follows:

- Frame 0:** Contains bits 0 through 28.
- Frame 1A:** Starts at bit 14, contains 'snoop' and 'resp'.
- Frame 2B:** Starts at bit 25, contains 'snoop' and 'resp'. It also includes 'Bdata' and 'EG J K'.
- Frame 3D:** Starts at bit 14, contains 'Ddata' and 'L'.
- Frame 4:** Starts at bit 14, contains 'F', 'C', 'Ack C', 'N', and 'snoop'.
- Frame 5:** Starts at bit 14, contains 'M', 'I', 'O', 'snoop', 'P', and 'Ack P'.
- Frame 6Q:** Starts at bit 14, contains 'Qdata' and 'H'.
- Frame 7:** Starts at bit 14, contains 'R ...'.

[illegible]